INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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First Named Inventor	Thomas Meredith BROWN
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Examiner Name	Thao P. LE
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	U.S. PATENTS						
Examiner Initials*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
	1						

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Examiner Initials*	Cite No	Foreign Document Number ³	Country Code ²	Kind Code⁴	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁵
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	2	0147044	wo	A2	2001-06-28	Plastic Logic Limited		Y
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	1	Francis GARNIER, et al., VERTICAL DEVICE ARCHITECTURE BY MOLDING OF ORGANIC-BASED THIN FILM TRANSISTOR, Applied Physics Letters, Volume 73, Number 12, September 21, 1998, pp. 1721-1723	Y		
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EXAMINER SIGNATURE						
Examiner Signature		Date Considered				

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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